

	Type	Hits	Search Text	DBs	Time Stamp
54	BRS	0	(bias adj direction) and (straying adj limit) and rout\$ and layout	USPAT	2001/02/06 09:22
55	BRS	0	bias and (straying adj limit) and rout\$ and layout	USPAT	2001/02/06 09:23
56	BRS	0	(straying adj limit) and rout\$ and layout	USPAT	2001/02/06 09:23
57	BRS	0	straying adj limit	USPAT	2001/02/06 12:13
58	BRS	0	interactive same router same analog same design same (IC or (integrated adj circuit))	USPAT	2001/02/06 12:14
59	BRS	0	interactive same router same analog same design	USPAT	2001/02/06 12:16
60	BRS	2	interactive same router and (analog same design)	USPAT	2001/02/06 12:17
61	BRS	636	adler.in.	USPAT	2001/02/06 12:18
62	BRS	0	interactive same router same analog same circuit	EPO, JPO; Derwent	2001/02/06 12:20
63	BRS	20123	interactive router	EPO, JPO; Derwent	2001/02/06 12:20
64	BRS	0	(analog adj circuit adj design) and (interactive adj router)	EPO, JPO; Derwent	2001/02/06 12:20
65	BRS	1	interactive adj router	EPO, JPO; Derwent	2001/02/06 12:20
66	BRS	14	analog adj circuit adj design	EPO, JPO; Derwent	2001/02/06 12:21
67	BRS	269	analog adj circuit adj design	USPAT	2001/02/06 12:22
68	BRS	36	interactive adj rout\$	USPAT	2001/02/06 12:22
69	BRS	0	(analog adj circuit adj design) and (interactive adj rout\$)	USPAT	2001/02/06 12:35